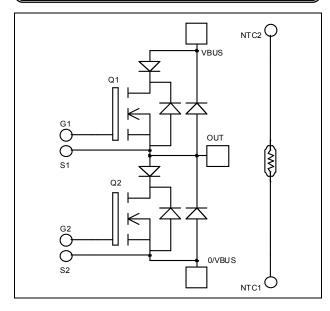


### Phase leg Serie & SiC parallel diodes Super Junction MOSFET Power Module



# $$\begin{split} V_{DSS} &= 800 V \\ R_{DSon} &= 150 m\Omega \ max \ @\ Tj = 25^{\circ}C \\ I_D &= 28A \ @\ Tc = 25^{\circ}C \end{split}$$

#### **Application**

- Motor control
- Switched Mode Power Supplies
- Uninterruptible Power Supplies

#### **Features**

- CoolMOSTM
  - Ultra low R<sub>DSon</sub>
  - Low Miller capacitance
  - Ultra low gate charge
  - Avalanche energy rated

#### • Parallel SiC Schottky Diode

- Zero reverse recovery
- Zero forward recovery
- Temperature Independent switching behavior
- Positive temperature coefficient on VF
- Kelvin source for easy drive
- Very low stray inductance
  - Symmetrical design
  - Lead frames for power connections
- Internal thermistor for temperature monitoring
- High level of integration

#### **Benefits**

- Outstanding performance at high frequency operation
- Direct mounting to heatsink (isolated package)
- Low junction to case thermal resistance
- Solderable terminals both for power and signal for easy PCB mounting
- Low profile
- RoHS Compliant

### All ratings @ $T_j = 25$ °C unless otherwise specified

#### **Absolute maximum ratings**

VBUS

O/VBUS

S2

Symbol	Parameter	Max ratings	Unit	
$V_{ m DSS}$	Drain - Source Breakdown Voltage		800	V
т	Continuous Drain Current	$T_c = 25$ °C	28	
$I_{D}$		$T_c = 80$ °C	21	A
$I_{DM}$	Pulsed Drain current	112		
$V_{GS}$	Gate - Source Voltage		±30	V
$R_{DSon}$	Drain - Source ON Resistance		150	mΩ
$P_{D}$	Maximum Power Dissipation $T_c = 25^{\circ}C$		277	W
$I_{AR}$	Avalanche current (repetitive and non repetitive)		17	A
$E_{AR}$	Repetitive Avalanche Energy		0.5	m I
$E_{AS}$	Single Pulse Avalanche Energy		670	mJ

OUT

OUT

NTC2

NTC1

CAUTION: These Devices are sensitive to Electrostatic Discharge. Proper Handling Procedures Should Be Followed. See application note APT0502 on www.microsemi.com

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### **Electrical Characteristics**

Symbol	Characteristic	Test Conditions		Typ	Max	Unit
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{GS} = 0V, V_{DS} = 800V$ $T_j = 25^{\circ}C$			50	
		$V_{GS} = 0V, V_{DS} = 800V$ $T_j = 125^{\circ}C$			375	μΑ
R <sub>DS(on)</sub>	Drain – Source on Resistance	$V_{GS} = 10V, I_D = 14A$			150	mΩ
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS} = V_{DS}$ , $I_D = 2mA$		3	3.9	V
$I_{GSS}$	Gate – Source Leakage Current	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0 \text{ V}$			±150	nA

**Dynamic Characteristics** 

•	Characteristic	Test Conditions	Min	Тур	Max	Unit
$C_{iss}$	Input Capacitance	$V_{GS} = 0V$		4507		
$C_{oss}$	Output Capacitance	$V_{\rm DS} = 25V$		2092		pF
$C_{rss}$	Reverse Transfer Capacitance	f=1MHz		108		
$Q_{\mathrm{g}}$	Total gate Charge	$V_{GS} = 10V$		180		
$Q_{\mathrm{gs}}$	Gate – Source Charge	$V_{Bus} = 400V$		22		nC
$Q_{gd} \\$	Gate – Drain Charge	$I_D = 28A$		90		
$T_{d(on)}$	Turn-on Delay Time	Inductive switching @125°C		10		
$T_{r}$	Rise Time	$V_{GS} = 15V$		13		
$T_{d(off)}$	Turn-off Delay Time	$V_{\text{Bus}} = 533 \text{V}$ $I_{\text{D}} = 28 \text{A}$		83		ns
$T_{\mathrm{f}}$	Fall Time	$R_G = 2.5\Omega$		35		
Eon	Turn-on Switching Energy	Inductive switching @ 25°C		291		1
$E_{\text{off}}$	Turn-off Switching Energy	$V_{GS} = 15V, V_{Bus} = 533V$ $I_D = 28A, R_G = 2.5\Omega$		278		μJ
$E_{on}$	Turn-on Switching Energy	Inductive switching @ 125°C $V_{GS} = 15V$ , $V_{Bus} = 533V$ $I_D = 28A$ , $R_G = 2.5\Omega$		510		T
E <sub>off</sub>	Turn-off Switching Energy			342		μJ
$R_{thJC}$	Junction to Case Thermal Resistance	)			0.45	°C/W

Series diode ratings and characteristics

Symbol	Characteristic	Test Conditions		Min	Typ	Max	Unit
$V_{RRM}$	Maximum Peak Repetitive Reverse Vo	ltage		1000			V
$I_{RM}$	Maximum Reverse Leakage Current	$V_R = 1000V$				250	μΑ
$I_F$	DC Forward Current		$T_c = 80^{\circ}C$		60		A
	Diode Forward Voltage	$I_F = 60A$		1.9 2.3		2.3	
$V_{\mathrm{F}}$		$I_F = 120A$			2.2		V
		$I_F = 60A$	$T_j = 125$ °C		1.7		
+	Reverse Recovery Time	$I_F = 60A$ $T_j = 25^{\circ}C$ $T_j = 125^{\circ}C$	$T_j = 25$ °C		290		ne
$t_{rr}$			$T_j = 125$ °C		390		ns
Qrr	Reverse Recovery Charge	$di/dt = 400A/\mu s$	$T_j = 25^{\circ}C$		1.34		μС
			$T_{j} = 125^{\circ}C$		4.7		μ
$R_{\text{thJC}}$	Junction to Case Thermal Resistance					0.65	°C/W

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Parallel diode ratings and characteristics

Symbol	Characteristic	Test Conditions		Min	Typ	Max	Unit	
$V_{RRM}$	Maximum Peak Repetitive Reverse Volt	age		1200			V	
$I_{RM}$	Maximum Reverse Leakage Current	mum Reverse Leakage Current   V <sub>z</sub> =1200V	$T_j = 25^{\circ}C$		64	400	μA	
$I_{\mathrm{F}}$	DC Forward Current		$T_{j} = 175^{\circ}C$ $T_{c} = 100^{\circ}C$		112 20	2000	A	
$V_{\mathrm{F}}$	Diode Forward Voltage	$I_F = 20A$	$T_i = 25^{\circ}C$ $T_j = 175^{\circ}C$		1.6	1.8	V	
Q <sub>C</sub>	Total Capacitive Charge	$I_F = 20A, V_R = 1200V$ di/dt = 1000A/ $\mu$ s			160		nC	
	Total Compaitance	Total Canacitance $f = 1 MHz, V_R = 200$	$f = 1MHz, V_R = 200V$			192		mE.
Q	Total Capacitance $f = 1MHz$ , $V_R = 400V$		= 400V		138		pF	
$R_{thJC}$	Junction to Case Thermal Resistance			-	1	°C/W		

Thermal and package characteristics

Symbol	Characteristic			Min	Max	Unit
$V_{ISOL}$	RMS Isolation Voltage, any terminal to case t =1 min, 50/60Hz			4000		V
$T_{J}$	Operating junction temperature range			-40	150	
$T_{JOP}$	Recommended junction temperature under switching conditions			-40	T <sub>J</sub> max -25	°C
$T_{STG}$	Storage Temperature Range			-40	125	
$T_{\rm C}$	Operating Case Temperature			-40	100	
Torque	Mounting torque	To Heatsink	M5	2.5	4.7	N.m
Wt	Package Weight				160	g

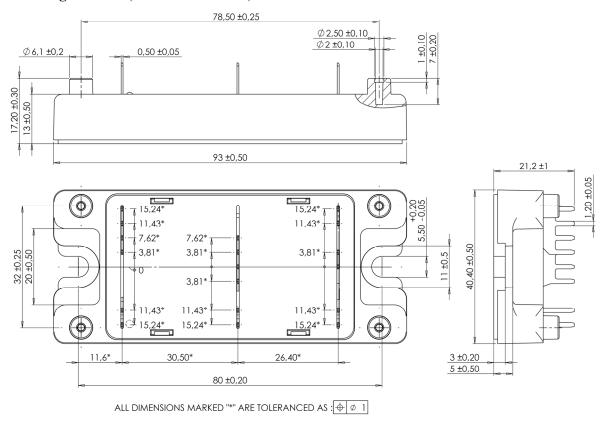
Temperature sensor NTC (see application note APT0406 on www.microsemi.com).

Symbol	Characteristic	Min	Typ	Max	Unit
R <sub>25</sub>	Resistance @ 25°C		50		kΩ
B <sub>25/85</sub>	$T_{25} = 298.15 \text{ K}$		3952		K

$$R_{T} = \frac{R_{25}}{\exp\left[B_{25/85}\left(\frac{1}{T_{25}} - \frac{1}{T}\right)\right]}$$
 T: Thermistor temperature R<sub>T</sub>: Thermistor value at T



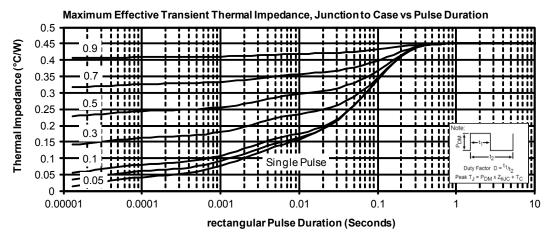
### **SP4 Package outline** (dimensions in mm)

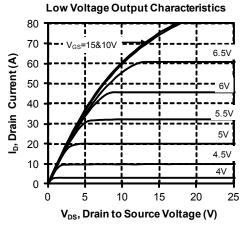


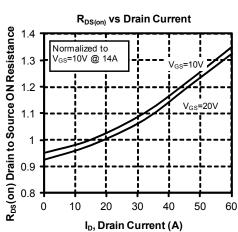
See application note APT0501 - Mounting Instructions for SP4 Power Modules on www.microsemi.com

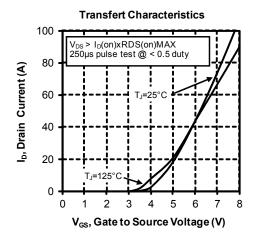


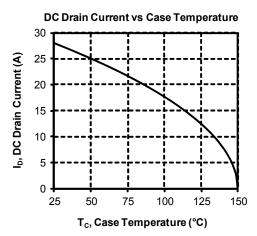
### **Typical CoolMOS Performance Curve**



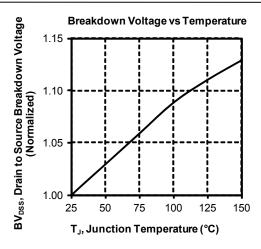


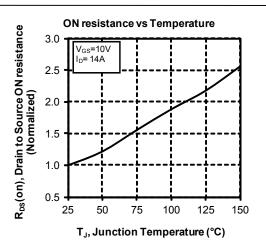


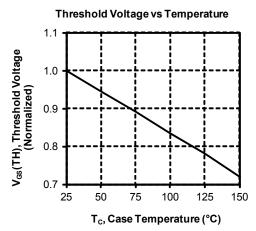


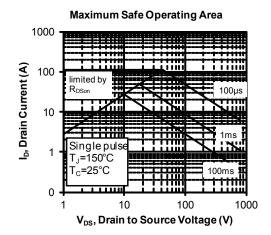


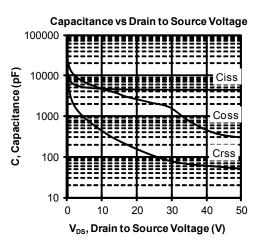


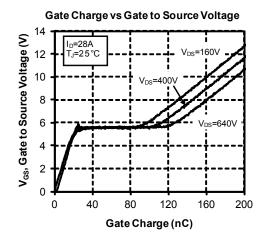




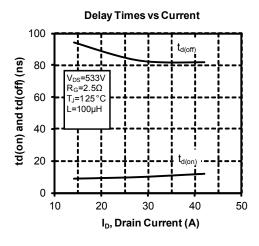


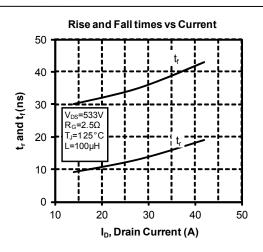


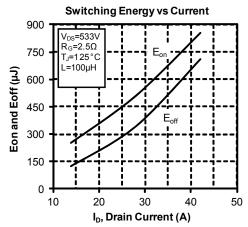


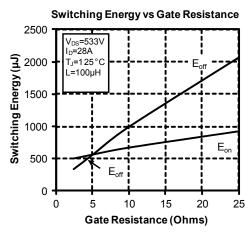


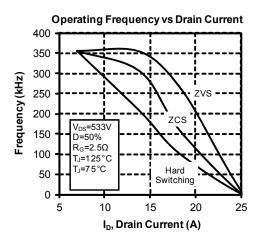


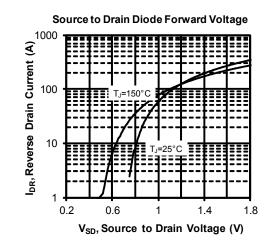






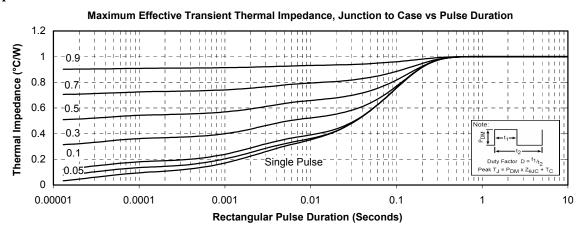


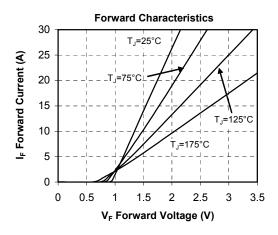


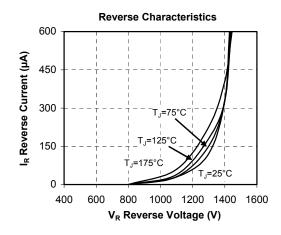


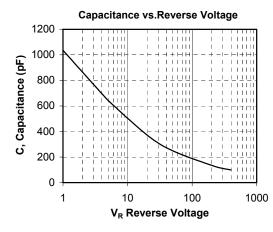


### **Typical SiC Diode Performance Curve**









"COOLMOSTM comprise a new family of transistors developed by Infineon Technologies AG. "COOLMOS" is a trademark of Infineon Technologies AG".



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